



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

843 39542X00

#8/Electr
2/28/02
Verner

Applicants: MINAMI et al.

Serial No.: 09/768,471

Filed: January 25, 2001

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
AND METHOD OF MANUFACTURING

Group: 2812

Examiner: R. Booth

RESPONSE TO ELECTION REQUIREMENT

February 11, 2002

Assistant Commissioner
for Patents
Washington, D.C. 20231

Sir:

In response to the election requirement dated January 9, 2002, applicants hereby elect Species I, directed to the embodiment shown in Fig. 1, without traverse. Applicants note the Examiner's indication that claim 1 is generic, and further note that claims 1-20 read on the elected species. In light of applicants' earlier election of the Group I, claims 1-11 and 15-21, without traverse, by the response filed on October 28, 2001, examination is respectfully requested on claims 1-11 and 15-20 which fall both within the elected species of Fig. 1 and the Group I previously elected.

If the Examiner believes that there are any other points

which may be clarified or otherwise disposed of either by telephone discussion or by personal interview, the Examiner is invited to contact applicants' undersigned attorney at the number indicated below.

It is respectfully requested that any shortage in the fee be charged to the account of Antonelli, Terry, Stout & Kraus, LLP, Account No. 01-2135 (Case No. 843.39542X00).

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP

By

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